PSION: Combining Logical Topology and Physical Layout Optimization for Wavelength-Routed ONoCs

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ABSTRACT
Optical Networks-on-Chip (ONoCs) are a promising solution for high-performance multi-core integration with better latency and bandwidth than traditional Electrical NoCs. Wavelength-routed ONoCs (WRONoCs) offer yet additional performance guarantees. However, WRONoC design presents new EDA challenges which have not yet been fully addressed. So far, most topology analysis is abstract, i.e., overlooks layout concerns, while for layout the tools available perform Place & Route (P&R) but no topology optimization. Thus, a need arises for a novel optimization method combining both aspects of WRONoC design. In this paper such a method, PSION, is laid out. When compared to the state-of-the-art design procedure, results show a 1.8× reduction in maximum optical insertion loss.

CCS CONCEPTS
• Theory of computation → Integer programming; • Hardware → Emerging optical and photonic technologies.

KEYWORDS
optical networks-on-chip; silicon photonics; physical layout; design optimization; placement & routing

1 INTRODUCTION
Optical Networks-on-Chip (ONoCs) have been proposed as a solution for the ever-increasing integration requirements of large System-on-Chip designs. Compared to traditional Electrical Networks-on-Chip, ONoCs present not only lower dynamic power consumption but also extremely low signal delay and higher bandwidth [9].

The use of light as opposed to electrical signals to send information between network nodes requires the following four main components on the optical routing plane: 1) modulators to convert electrical signals into optical signals at every node (electrical-optical interface) of the optical network, 2) demodulators to do the opposite, 3) waveguides acting as optical wires and 4) optical routing elements to transfer optical signals between waveguides [7].

ONoCs can be organized into two main categories: 1) active networks [3, 12, 17] and 2) passive networks. Active networks require a control layer for routing. Passive networks use routing elements which resonate with different frequencies such that a message is passively routed according to the wavelength of the carrier light. Hence, a message’s path is completely defined, at design time, by its origin.
and wavelength alone (Figure 1 shows an example of wavelength routing). Thus, passive ONoCs are also termed Wavelength-Routed ONoCs (WRONoCs) [8]. This eliminates network delay resulting from path setup and dynamic power consumption required for the extra control layer.

Multiple light sources of different wavelengths can be used to transmit separate information streams on the same waveguide without interference (wavelength-division multiplexing). This enables conflict-free communications with increased bandwidth. The only requirement is to make sure at design time that no two messages with the same wavelength are allowed to share the same waveguides.

The optical switching element in ONoCs is the Micro-Ring Resonator (MRR). It has a circular silicon structure whose radius defines the resonance frequency. A light signal with a certain wavelength propagating on a waveguide close to an MRR with a matching resonance frequency will be coupled to the MRR and moved onto another waveguide also close to that MRR [10]. Figure 2 shows an example of this behaviour.

The design of a WRONoC router is an optimization process with two aspects to consider: the logical topology and the physical layout of the router. The former assigns a wavelength to each message and each MRR and also connects the nodes through waveguides and MRRs such that the communication matrix, which specifies the communication requirements between nodes, is fulfilled. The latter optimally places and routes those elements on the optical plane while considering the physical positions of the nodes and constraints related to the physical placement of the waveguides.

So far both aspects have only been considered separately or with restrictions. Various works have presented specific topologies with few concerns about their layout [7, 13, 14]. Ramini et al. [11] present a topology designed in tandem with placement constraints, yet it results from a manual optimization effort for one set of node positions. Ortín-Obón et al. [9] take into consideration physical constraints, but analyze only the ring topology. Few attempts to optimize for non-complete communication matrices [1, 5]. P&R tools to optimize the second aspect have been developed [2, 15, 16], but all take a topology as input, forcing the designer to choose the topology beforehand.

However, neither aspect can be considered in isolation, as each influences the other [11, 13, 15]. During generation of the logical topology we are unable to accurately predict important physical characteristics, e.g. the number of waveguide crossings, of the final design after P&R. Furthermore, during P&R, if the logical topology has already been chosen and fixed, any subsequent optimization is being done only around a local minimum of the solution space.

Ideally, a design tool would take as inputs the communication matrix and the physical positions of the nodes and, by working on both aspects simultaneously, produce a fully-optimized fully-custom logical topology and matching physical layout [13]. In reality, the problem space of such an optimization is discouragingly vast for any but the simplest cases. Thus, in this paper we propose and solve a constrained version of the complete problem. In this version – PSION – a physical layout template is also given as an input to the optimization. The template mainly consists of MRR placeholders and waveguides already placed and routed on the optical plane, and connects all nodes.

We define the optimization problem in Section 2. Physical layout templates are described in Section 3 and the Mixed Integer Programming (MIP) model used to optimize them is presented in Section 4. Section 5 explains a fast technique to verify the model’s feasibility and Section 6 then proposes a 3-step algorithm to efficiently solve it. Finally, Section 7 reveals three layout templates and tests them against the state-of-the-art P&R PROTON+ [15] and PlanarONoC [2] tools.

2 WRONOC DESIGN PROBLEM

We formally define the optimization problem for the design of WRONoC routers as follows:

**Input data:**
- Communication matrix: a square binary matrix $CM_{i,j} \in \mathbb{R}^{N \times N}$ with $N$ equal to the number of nodes and where $CM_{i,j} = 1$ if node $i$ sends a message to node $j$.
- Physical positions of the modulators and demodulators of each node on the optical plane.
- Technology parameters: power loss values.

**Output data:**
- Wavelength of each message and MRR.
- Placement of each MRR.
- Routing of each waveguide.

**Minimization objectives.** Their choice depends on the technology and the needs of the design. We consider 1) number of wavelengths, 2) message insertion loss and 3) number of MRRs, as in previous publications [9–11, 13–15]. With PSION, the weighting coefficient for each objective can be freely adjusted to meet different designer demands.

Message insertion loss is the sum of seven types of losses: 1) crossing loss, 2) drop loss, 3) through loss, 4) bending loss, 5) propagation loss, 6) modulator loss and 7) demodulator loss [6, 15]. We consider all except the last two, which are constant and equal for all messages and thus can be ignored from an optimization perspective.

3 PHYSICAL LAYOUT TEMPLATE

We consider a constrained version of the complete problem, where an extra input is required. This input, called a physical layout template, consists of a collection of WRONoC router elements.
Figure 3: Generalizing the 4x4 GWOR topology [14] using endpoints, GRUs and waveguide sections.

(modulators, demodulators, waveguides and MRR placeholders) already placed and routed on the optical plane.

The role of the solver with this new input is to optimally route the messages defined in the communication matrix through the template and to activate the necessary routing features for the chosen paths.

This way we significantly reduce the complexity of the complete problem while still improving upon the state-of-the-art solutions. Nevertheless, this template does not need to be intricate or sophisticated. In fact, the intuitive knowledge of the designer about the structure of the router to be created is more than enough to provide a good template.

3.1 Template elements

We model layout templates with three layout elements. Together they allow for the design of any WRONoC topology (an example is shown in Figure 3).

Endpoints represent modulators and demodulators. They are placed wherever the (de)modulators for each node are and connect to one waveguide section.

General Routing Units (GRUs) are elements that connect to multiple waveguide sections (the edges of the GRU) and contain MRR placeholders, to be populated by the solver as needed. They are the routing building blocks of the template and are described further in the next section.

Waveguide sections connect two GRUs or a GRU and an endpoint. Each section has two associated parameters: length and extraloss. The latter is used to describe sections with other constant sources of insertion loss besides length, such as sections with 90° bends.

Our method can solve for any template, i.e., any arrangement of endpoints, GRUs and waveguide sections.

3.2 General Routing Unit

Photonic Switching Elements (PSEs) are commonly applied in WRONoC routers [7, 11, 13, 14]. For PSEs, MRR locations and wavelengths are explicitly specified and the waveguide structure is fixed.

GRUs are the routing building blocks for the proposed layout template and, in contrast to PSEs, GRUs are not inherently constrained to a specific internal structure. Instead, only MRR placeholders are predefined in a GRU. Thus, different MRR placement and wavelength configurations can happen for each GRU, as well as different edge connection arrangements. This provides more flexibility in the resulting WRONoC design.

3.2.1 Structure. Figure 4(a) shows the structure of a GRU: the four waveguide sections form a crossing where any of the four corners on that crossing can have an MRR. Sometimes the crossing can be avoided, leading to the variations in Figure 4(b).

We also consider an additional structure variation called corner bending. When active, the GRU contains no MRRs and some corners may be replaced by a bend between the two edges in that corner, as in Figure 4(c).

Note that two corners connected to the same edge of a GRU cannot be both bent. Therefore, if two edges are connected through a corner bend, the other two edges must be bent through the opposite corner if they have messages going through. Figure 4(d) shows two invalid configurations.

This extra variation proves useful for sparser templates (low ratio of the number of messages to the number of MRR positions), or in cases where multiple messages must be routed through the same corner.
3.2.2 Routing. Figure 5 shows the routing possibilities through a GRU. If no MRRs of the same wavelength as the message are present and corner bending is not activated, the message will have no direction change, as shown in Figure 5(a).

For wavelength routing, the message can be routed through an MRR with the same wavelength in the closest corner, as shown in Figure 5(b), or in the opposite corner, as shown in Figure 5(c).

With corner bending, since the two waveguides become connected, all messages in any of the two waveguides are routed through that corner, regardless of wavelength, as shown in Figure 5(d).

A message’s path through a GRU is always independent of its direction, i.e., all routing features are bidirectional. Also, the four MRRs on a GRU can have different wavelengths (examples are shown in Figure 1). This allows for intricate multi-message routing capabilities per waveguide crossing which have not yet been optimized to their full potential.

3.3 Communication Matrix

Given a layout template, the communication matrix can be translated into a set of messages (one for each nonzero entry), where each message is associated with two endpoints on that template: the sender and the receiver.

4 MATHEMATICAL MODEL

We solve the constrained problem using a Mixed Integer Programming model. Advantages of MIP models include:

(1) A MIP model can give optimal solutions, or at least an upper/lower bound to the optimal value of the optimization function.

(2) The same MIP can be used to optimize different objectives, therefore giving the designer more flexibility.

(3) MIP models are flexible, so new GRU designs, routing features or other modifications can easily be added.

The model constants and indices are outlined in Table 1. Constants $L_{wg}$, $L_{wg}^l$ and indices $W_g^l$ collectively describe the physical layout template and indices $E_m$ define the communication matrix. Table 2 lists all model variables.

We now specify the constraints and the optimization function (note that similar constraints for multiple directions or corners are omitted). Finally, we present some model reduction techniques.

4.1 Constraints

Message routing. A path with the correct beginning and end must be guaranteed for each message. For that we apply the following three sets of constraints:

(1) A message must be on the waveguide of the endpoints it is sent from and received by.

$$m_{wg_m,w}^{E_1}_m = 1 \quad m_{wg_m,w}^{E_2}_m = 1 \quad \forall m = 1...N_m$$

(2) If an endpoint does not send or receive a given message, that message cannot be present on its waveguide.

$$m_{wg_m,w}^{E_2}_{ep} = 0 \quad \forall ep = 1...N_{ep} \setminus \{E_m, E_w\} \quad \forall m = 1...N_m$$

(3) A message is exactly on 0 or 2 edges of a GRU.

$$m_{wg_m,w}^{E_1}_{1} + m_{wg_m,w}^{E_2}_{1} + m_{wg_m,w}^{E_1}_{2} + m_{wg_m,w}^{E_2}_{2} \in \{0, 2\} \quad \forall m = 1...N_m, g = 1...N_{gru}$$

It is possible for a message to be on all four edges of a GRU, but this was neglected because it appearing on an optimized solution is highly unlikely, and not including it simplifies the model and the problem space. The reason is that a message routing through all 4 edges (enter through edge 1, leave through 2, enter through 3, leave through 4) can also route through 2 edges (enter through 1, leave through 4) with half the loss on that GRU and a shorter path.

Wavelength exclusion. Each waveguide section has at most one wavelength. For wavelength routing, the message can be routed through an MRR on a GRU. If no MRRs of the same wavelength as the message are present and corner bending is not activated, the message will have no direction change, as shown in Figure 5(a).

Activation of routing features. A path is chosen for each message but, to make that path take effect, constraints are needed to enforce the activation of the routing features responsible for it.

If a message takes the direct path through a GRU, no features need to be turned on. However, if a message is present on adjacent edges of a GRU, then one of the three options from Figure 5(b-d)
Table 2: Model variables

<table>
<thead>
<tr>
<th>Binary</th>
<th>Continuous</th>
</tr>
</thead>
<tbody>
<tr>
<td>cbₔ,p</td>
<td>milₘ</td>
</tr>
<tr>
<td>wluₔ,λ</td>
<td>maxil</td>
</tr>
<tr>
<td>mweₘ₁,ₘ₂</td>
<td></td>
</tr>
<tr>
<td>mwgₘ₁,ₘ₉</td>
<td></td>
</tr>
<tr>
<td>clₘ₁,ₘ₂, bₘ₁,ₘ₂</td>
<td></td>
</tr>
<tr>
<td>tlₘ₁,ₘ₂</td>
<td></td>
</tr>
<tr>
<td>rumₘ₁,ₘ₂</td>
<td></td>
</tr>
<tr>
<td>ruₘ₁,ₘ₂</td>
<td></td>
</tr>
<tr>
<td>mchₘ₁, mcvₘ₁</td>
<td></td>
</tr>
</tbody>
</table>

Must be active:

\[
\text{mwg}_{m, w}^T \land \text{mwg}_{m, w}^R \Rightarrow \text{rum}_{m, T L, m} \lor \text{rum}_{m, B R, m} \lor \text{cb}_{m, T L} \\
\forall 4 \text{ corners, } m = 1 \ldots N_m, g = 1 \ldots N_{gru}
\]

Each MRR can only be used for one message. The following constraints both set the value of \( ruₘ₁,ₘ₂ \) and enforce that restriction:

\[
ruₘ₁,ₘ₂ = \sum_{m=1}^{N_m} \text{rum}_{m, p, m} \quad \forall g = 1 \ldots N_{gru}, p \in P
\]

Corner bending. The following three sets of constraints are required:

(1) A GRU cannot have corners bent and MRRs active.

\[
\text{cb}_{g, p_1} + \text{ru}_{g, p_2} \leq 1 \quad \forall p_1, p_2 \in P, g = 1 \ldots N_{gru}
\]

(2) Corners for the same message cannot be bent at the same time for the same GRU.

\[
\text{cb}_{g, T L} + \text{cb}_{g, T R} \leq 1 \quad \text{cb}_{g, T R} + \text{cb}_{g, B R} \leq 1 \\
\text{cb}_{g, T L} + \text{cb}_{g, B L} \leq 1 \quad \text{cb}_{g, B L} + \text{cb}_{g, B R} \leq 1 \\
\forall g = 1 \ldots N_{gru}
\]

(3) If a corner is bent then messages present on one of the edges of that corner must be present on the other.

\[
\text{cb}_{g, T L} \Rightarrow \text{mwg}_{m, w}^T = \text{mwg}_{m, w}^R \\
\forall 4 \text{ corners, } m = 1 \ldots N_m, g = 1 \ldots N_{gru}
\]

Crossing loss. A message suffers crossing loss when going through a crossing with a perpendicular waveguide. Two things must happen for a message to have crossing loss on a GRU: (1) the message must take a direct path through the GRU and (2a) the perpendicular direct path must be taken by at least one other message or (2b) there must be at least one message taking the path on Figure 4(c). For any other case the crossing on the GRU can be avoided, as exemplified in Figure 4(b), and no crossing loss exists.

First set the values of the variables \( mchₘ₁, mcvₘ₁ \):

\[
\text{mwg}_{m, w}^T \land \text{mwg}_{m, w}^R \land \text{rum}_{g, B R, m} \Rightarrow \text{mch}_{g} \land \text{mcv}_{g} \\
\forall 4 \text{ corners, } m = 1 \ldots N_m, g = 1 \ldots N_{gru}
\]

The value of \( clₘ₁ \) follows:

\[
\text{mwg}_{m, w}^T \land \text{mwg}_{m, w}^R \land \text{cb}_{g, T L} \Rightarrow \text{cl}_{g, m} \\
\forall 2 \text{ directions, } m = 1 \ldots N_m, g = 1 \ldots N_{gru}
\]

Through loss. If a message is going through the direct path on a GRU, then it has through loss for each MRR present on that GRU.

\[
\text{mwg}_{m, w}^T \land \text{mwg}_{m, w}^R \land \text{ru}_{g, p} \Rightarrow \text{tl}_{g, p, m} \\
\forall 2 \text{ directions, } m = 1 \ldots N_m, p \in P, g = 1 \ldots N_{gru}
\]

Bending loss. A message has bending loss on a GRU if it routes through a corner that is bent.

\[
\text{mwg}_{m, w}^T \land \text{mwg}_{m, w}^R \land \text{cb}_{g, T L} \Rightarrow \text{bl}_{g, m} \\
\forall 4 \text{ corners, } m = 1 \ldots N_m, g = 1 \ldots N_{gru}
\]

Drop loss. Proportional to the number of MRRs used by each message.

Propagation loss. Proportional to the length of the waveguides the message goes through.

Message insertion loss. The total insertion loss of a message over all waveguides and GRUs is a weighted sum.

\[
milₘ = \sum_{i=1}^{N_{wg}} \left( L_i^p + L_i^E \right) \ast \text{mwg}_{m, i} + L_i^T \ast \sum_{g=1}^{N_{gru}} \sum_{p \in P} \text{tl}_{g, p, m} \\
+ \sum_{g=1}^{N_{gru}} \left( L_i^C \ast \text{cl}_{g, m} + L_i^B \ast \text{bl}_{g, m} + L_i^D \ast \sum_{p \in P} \text{rum}_{g, p, m} \right) \\
\forall m = 1 \ldots N_m
\]

1This feature can be turned off, if needed, by adding constraints to set all \( cb_{g, p} \) variables to zero.
4.2 Objective function
Calculating the number of wavelengths is done with the following constraints:

\[ wlu_\lambda \geq mw_{m,\lambda} \quad \forall m = 1...N_m, \lambda = 1...N_\lambda \]
\[ nw_l = \sum_{\lambda=1}^{N_\lambda} wlu_\lambda \]

Determining the maximum insertion loss over all messages is done with the following constraints:

\[ \text{maxil} \geq m\text{il}_m \quad \forall m = 1...N_m \]

Finally, the following objective function is minimized:

\[ \alpha_1 * nwl + \alpha_2 * \text{maxil} + \alpha_3 * \sum_{m=1}^{N_m} m\text{il}_m + \alpha_4 * \sum_{\gamma=1}^{nwl} \sum_{p \in \mathcal{P}} ru_{\gamma,p} \]

where \( \alpha_i \) are optimization weights chosen by the designer.

Since the value for the insertion loss of each message is available through the \( m\text{il}_m \) variables, functions other than the maximum or the sum of the insertion loss can also be added to the model and used for optimization.

4.3 Model reduction techniques
4.3.1 Restrictions on usage of wavelengths. The following constraints can be added:

\[ mw_{m,\lambda} = 0 \quad \forall \lambda = (m+1)...N_\lambda \quad \forall m = 1...N_m \]

They restrict the possible wavelengths for each message: message 1 uses wavelength 1, message 2 uses wavelengths 1 or 2, etc. This way, some meaningless variations around the same effective solution are removed. The optimal solution, however, is not removed from the solution space.

4.3.2 Restrictions on usage of MRRs. Empirically we find that minimizing the insertion loss favors optimal solutions where each message uses a low total number of MRRs. Following this reasoning, constraints can be added to the model that force a maximum number of MRRs per message (\( R^{\text{max}} \)):

\[ \sum_{\gamma=1}^{N_{\gamma}} \sum_{p \in \mathcal{P}} ru_{\gamma,p} \leq R^{\text{max}} \quad \forall m = 1...N_m \]

This reduces the set of paths considered by the solver by removing poor, convoluted paths while keeping the more direct paths between endpoints.

5 PROOF OF FEASIBILITY
It is possible that the chosen layout template cannot satisfy the entire communication matrix (for example, if the template is too small). For those cases, the model above will be unfeasible. Verifying the existence of a solution can be done much faster using a simplified version of the model. For that we consider \( N_\lambda = N_m \) and uniquely assign a wavelength to each message by adding these constraints:

\[ mw_{m,\lambda} = 1 \quad \forall m = 1...N_m, \lambda = m \]
\[ mw_{m,\lambda} = 0 \quad \forall m = 1...N_m, \lambda \neq m \]

The resulting model can be solved much faster but, if the solver is unable to find a feasible solution for this simplified model, the complete model is also unfeasible.

\[ \text{Proof.} \quad \text{Assume a feasible solution exists. It will have } nwl \leq N_m. \]

From that solution build another where each message uses its own wavelength (thus either maintaining or increasing \( nwl \)). Any message that changes its wavelength must also change the wavelength of the MRRs it uses. This is always possible because each MRR routes only one message. Furthermore, the wavelength exclusion rule is always satisfied. Hence, the feasibility of the complete model implies the existence of a solution for the simplified version. \( \square \)

6 3-STEP OPTIMIZATION
Section 4 introduced a MIP model that is capable of solving the constrained problem for any layout template. Therefore, programming the model as presented on any MIP solver and solving it directly for the chosen minimization objective is enough to obtain the optimal solution. However, due to the nature of the problem, it is possible to slightly alter the optimization process yielding more control and faster results. This leads to the proposed 3-step optimization process used in PSION, where each step optimizes a slightly different version of the model and produces a solution used at the start of the next step.

In the first step we consider \( N_\lambda = N_m \) and apply the feasibility proof from Section 5. In this way we can generate the first feasible solution much faster if one exists. It can then be used as a warm start, which decreases optimization times substantially. This has the added bonus of stopping the process as quickly as possible if unfeasible.

In the second step we only minimize the number of wavelengths, for two reasons. Firstly, the designer will most likely want to use fewer wavelengths than the number of messages, thus making this optimization problem hierarchical, i.e., minimizing wavelengths has a higher priority than minimizing insertion loss or \#MRRs. Secondly, because, after completing this step, a feasible solution for a smaller number of wavelengths is then available, so the model can again be simplified by eliminating from it the \( N_m - nwl \) unused wavelengths. To make this simplification, the following constraints are added:

\[ mw_{m,\lambda} = 0 \quad \forall m = 1...N_m, \text{ unused wavelengths } \lambda \]

The designer might be willing to use more wavelengths than the minimum needed. In that case it is up to the designer to know the maximum acceptable number of wavelengths. The second step can be stopped earlier once a solution is found within that acceptable range.

In the third step we consider the model with the needed amount of wavelengths only and further optimize the last solution using the chosen function (\( \text{maxil} \), for example). We have now reached the final solution.

Using this process we can notably simplify the problem space during the optimization. However, because the model reductions are always done according to the hierarchical characteristics of the optimization goals, the optimal solution is never missed.
7 RESULTS

The MIP model and 3-step optimization algorithm are programmed in C++ and make use of Gurobi [4], a MIP solver, on a 2.6 GHz CPU.

We tested our model and optimization procedure against the state-of-the-art PROTON+ and PlanarONoC P&R tools. Most of their result analysis is dedicated to an 8 node test case with 44 messages. We solved the same test case considering the same communication matrix, node placement, die size, crossing size and loss parameters.

PROTON+ and PlanarONoC compare results originating from P&R of three logical topologies (8x8 λ-Router, 8x8 GWOR and 8x8 Standard Crossbar). PROTON+ also considers five different sets of node positions and various permutations of solver parameters, which results in a range of values for the results.

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7.1 Physical templates

All templates share some common features:

1. Each node has two endpoints: a modulator and a demodulator.
2. The power distribution network – not shown in these templates – can always be routed from the outside such that no other crossings in the router exist besides those considered by the template.

The centralized grid template is a \( w \times h \) grid of GRUs where \( w + h \) equals the number of nodes. Each node is connected with waveguides to two ports on the grid (one for sending, the other for receiving), which are next to each other. This router can be thought of as a different generalization of the 4x4 GWOR router in Figure 3. The grid itself was placed on the center of the die, the ports used by each node were chosen as to remove any crossings external to the grid and the waveguides connecting the nodes to the grid were manually routed to minimize bends.

The distributed grid template was built by placing horizontal or vertical pairs of waveguides starting at each node, with a GRU on each crossing.

The custom template was built specifically for this test case (i.e., these node positions and communication matrix). In particular, no message needs to use more than one MRR. Therefore, \( R_{\text{max}} \) was set to 1 for this template while the grid templates were solved with \( R_{\text{max}} = 2 \).

7.2 Comparison to the state-of-the-art

Figure 1 shows the result for the centralized grid router and Table 3 presents the various comparisons. Most important are the number of wavelengths and maximum insertion loss, but #MRRs and execution time are also given. Results from PSION are optimal solutions for the given templates.

Table 3: Results for 8 nodes, 44 messages

<table>
<thead>
<tr>
<th></th>
<th>#WLs</th>
<th>Max IL</th>
<th>#MRRs</th>
<th>Time</th>
</tr>
</thead>
<tbody>
<tr>
<td>PROTON+</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>λ-Router</td>
<td>8</td>
<td>6.6 - 9.0</td>
<td>56</td>
<td>134</td>
</tr>
<tr>
<td>GWOR</td>
<td>7</td>
<td>8.1 - 11.3</td>
<td>48</td>
<td>79</td>
</tr>
<tr>
<td>Std. crossbar</td>
<td>8</td>
<td>10.5 - 13.0</td>
<td>64</td>
<td>602</td>
</tr>
<tr>
<td>PlanarONoC</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>λ-Router</td>
<td>8</td>
<td>5.2</td>
<td>56</td>
<td>&lt;1</td>
</tr>
<tr>
<td>GWOR</td>
<td>7</td>
<td>6.4</td>
<td>48</td>
<td>&lt;1</td>
</tr>
<tr>
<td>Std. crossbar</td>
<td>8</td>
<td>7.4</td>
<td>64</td>
<td>&lt;1</td>
</tr>
<tr>
<td>PSION</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Centralized</td>
<td>8</td>
<td>3.1</td>
<td>52</td>
<td>178</td>
</tr>
<tr>
<td>Distributed</td>
<td>8</td>
<td>3.6</td>
<td>48</td>
<td>37</td>
</tr>
<tr>
<td>Custom</td>
<td>7</td>
<td>4.1</td>
<td>40</td>
<td>&lt;1</td>
</tr>
</tbody>
</table>

\( T_{\text{opt}} \) is time to find the optimal solution, \( T_{\text{total}} \) is total execution time (for PSION: \( T_{\text{total}} = T_{\text{opt}} + \text{time to prove optimality} \); for others: the time that produces the best result).

Time in seconds, insertion loss in dB.

Number of wavelengths. The communication matrix in these tests requires an absolute minimum of 7 wavelengths when using one modulator per node. The custom template matches this value, but the grid templates require an actual minimum of 8. However, PSION can reduce this number if given a smaller communication matrix, in contrast to the presented logical topologies.
We also solved the MIP models from these tests by directly mini-
with PROTON+. PlanarONoC is still two orders of magnitude faster.
The custom template is much better, however, mostly because of

8 CONCLUSION
The 3-step procedure is
faster on average. Likewise, we ran the
same tests without any of the reduction techniques from Section 4.3.4.

Further comments
We also solved the MIP models from these tests by directly mini-
and the use of the MIP models was much faster in average.
Finally, the grid templates are entirely straightforward and can be
used in virtually any WRONoC, which speaks to the potential of

7.3 Further comments
We also solved the MIP models from these tests by directly mini-
and the use of the techniques from Section 4.3.

Faster optimization techniques. Finally, template synthesis methods
should also be explored.

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Max. insertion loss. PSION produces results that are 2.7× better
compared to PROTON+ and 1.8× better compared to PlanarONoC.
Some intuitive reasons are available to justify these outcomes:
• We combined logical topology and physical layout optimization.
• We used templates, which automatically removes many sub-
optimal solutions compared to a conventional P&R solution
space.
• We used GRUs, which support up to four MRRs per crossing,
whereas PSEs only support two. Thus, fewer GRUs are used
in our templates than PSEs are used in logical topologies such as
the λ-Router. This increases the density of our designs
which decreases the total number of crossings.
• We drastically reduced the number of crossings outside
PSEs/GRUs.
• We obtain the optimal solution within the specified template.

MRR usage. This was not an optimization objective in these tests,
but the comparison to both PROTON+ and PlanarONoC remains
favourable.

Time. Grid templates have a total execution time comparable
with PROTON+. PlanarONoc is still two orders of magnitude faster.
The custom template is much better, however, mostly because of
the technique from Section 4.3.2.

Furthermore, the optimal solution is consistently reached in half
or less than the total execution time. Thus, a designer not requiring
proof of optimality can end the optimization once a satisfactory
solution is found which, based on these results, is likely to appear
quickly and be close to optimal.

8 CONCLUSION
In this work we defined the WRONoC design problem and pre-
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